

**IN THE CLAIMS**

1. (currently amended): A semiconductor device, comprising:

exposed fuse terminals provided ~~[[on]]~~ adjacent a chip substrate surface; and  
an exposed discharge contribution terminal which is provided ~~at the upper side on~~  
adjacent said chip substrate surface; ~~and of which the~~

wherein a first height, from said chip substrate surface to ~~[[the]]~~ a top face of said  
discharge contribution terminal, is higher than ~~[[the]]~~ a second height, ~~[[of]]~~ from said chip  
substrate surface to ~~[[the]]~~ a top face of said fuse terminals.

2. (original): The semiconductor device according to Claim 1, wherein said discharge contribution terminal is a chip terminal.

3. (original): The semiconductor device according to Claim 1, wherein said discharge contribution terminal is a dummy terminal which is disposed so as to surround the fuse terminals.

4. (currently amended): The semiconductor device according to Claim ~~[[3,]]~~ 5, wherein said dummy terminal is electrically connected to a reference voltage power supply.

5. (new): The semiconductor device according to Claim 1, comprising a protective circuit, and wherein said discharge contribution terminal is connected to said protective circuit.

6. (new): The semiconductor device according to Claim 1, wherein said fuse terminals and said discharge contribution terminal comprise respective bumps that are disposed on a top surface of said semiconductor device.

7. (new): A semiconductor device, comprising:

fuse terminals provided on a chip substrate surface; and

a discharge contribution terminal which is provided at the upper side on said chip substrate surface and of which the height from said chip substrate surface to the top face is higher than the height of the top face of said fuse terminals;

wherein said discharge contribution terminal is a dummy terminal which is disposed so as to surround the fuse terminals.

8. (new): The semiconductor device according to Claim 7, wherein said dummy terminal is electrically connected to a reference voltage power supply.